

## Refine Search

### Search Results -

Term	Documents
TRANSISTOR	689162
TRANSISTORS	402706
ON	1280449
ONS	185961
STATE	3542384
STATES	3016172
((TRANSISTOR ADJ ON) ADJ STATE).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	0
(TRANSISTOR ADJ ON ADJ STATE ).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	0

Database:

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

Search:

L28

Refine Search

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### Search History

DATE: Tuesday, October 11, 2005   [Printable Copy](#)   [Create Case](#)

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Name   Query  
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 side

Hit  
Count   Set  
                     Name  
                     result  
                     set

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ

<u>L28</u>	transistor adj ON adj state	0	<u>L28</u>
<u>L27</u>	L13 and ON adj state	0	<u>L27</u>
<u>L26</u>	117 and incomplete adj ON adj state	0	<u>L26</u>

*DB=USPT; PLUR=YES; OP=ADJ*

L25 117 and incomplete adj ON adj state 0 L25

*DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ*

L24 123 and hold\$3 adj potential 0 L24

L23 L22 and @py<=2002 23 L23

L22 scan\$4 adj line\$1 same (intermediate or middle or variable) adj potential 42 L22

L21 scan\$4 adj line\$1 same (intermediate or middle or variable) adj potential 42 L21

*DB=USPT; PLUR=YES; OP=ADJ*

L20 scan\$4 adj line\$1 same (intermediate or middle or variable) adj potential 15 L20

*DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ*

L19 L17 and @py<=2001 91 L19

L18 L17 and @py<=2002 167 L18

L17 l3 and pixel\$1 same data adj lines same scan\$4 adj lines same (TFT or TFTs or thin adj film adj transistor\$1) same substrate\$1 645 L17

L16 L13 and substrate\$1 885 L16

L15 L13 and substrate1 0 L15

L14 L13 and ON and OFF 2 L14

L13 l3 and pixel\$1 same data adj lines same scan\$4 adj lines same (TFT or TFTs or thin adj film adj transistor\$1) 885 L13

L12 l3 and pixel\$1 same data adj lines same scan\$4 adj lines 1110 L12

L11 L9 and active adj matrix 2 L11

L10 L9 and display\$1 11 L10

L9 (TFT or thin adj film adj transistor\$1) same ON same OFF 129 L9

L8 scan adj line\$1 same (TFT or thin adj film adj transistor\$1) same ON same OFF 0 L8

L7 L1 and scan adj line\$1 same (TFT or thin adj film adj transistor\$1) same ON same OFF 0 L7

L6 L5 and @py<=2002 13 L6

L5 L2 and (TFT or thin adj film adj transistor\$1) and ON and OFF 20 L5

L4 L2 and (TFT or thin adj film adj transistor\$1) same ON same OFF 0 L4

L3 L2 and (TFT or thin adj film adj transistor\$1) 11766 L3

L2 l1 and active adj matrix same substrate 15438 L2

L1 display\$1 1967850 L1



END OF SEARCH HISTORY

## Freeform Search

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<b>Database:</b>	US Pre-Grant Publication Full-Text Database
	<b>US Patents Full-Text Database</b>
	US OCR Full-Text Database
	EPO Abstracts Database
	JPO Abstracts Database
	Derwent World Patents Index
	IBM Technical Disclosure Bulletins

<b>Term:</b>	111 and display\$3	 
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<b>Display:</b>	20	<b>Documents in Display Format:</b>	TI	<b>Starting with Number</b>	1
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**Generate:** ☐ Hit List ☒ Hit Count ☐ Side by Side ☐ Image

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### Search History

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<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>			
<u>L12</u>	111 and display\$3	22	<u>L12</u>
<u>L11</u>	18 and @py<=2002	255	<u>L11</u>
<u>L10</u>	L5 and (intermediate or middle) and (potential or voltage) and transistor adj ON	0	<u>L10</u>
<u>L9</u>	L5 and (intermediate or middle) and (potential or voltage) same transistor adj ON	0	<u>L9</u>
<u>L8</u>	L5 and (intermediate or middle) and (potential or voltage)	255	<u>L8</u>
<u>L7</u>	L5 and (intermediate or middle) and potential or voltage	2138483	<u>L7</u>
<u>L6</u>	L5 and (intermediate or middle) and potential or voltage	2138463	<u>L6</u>
<u>L5</u>	L4 and transistor same ON same state	1089	<u>L5</u>
<u>L4</u>	transistor same ON	2135	<u>L4</u>
<u>L3</u>	transistor same ON adj state	0	<u>L3</u>
<u>L2</u>	transistor same partial adj ON adj state	0	<u>L2</u>
<u>L1</u>	transistor same incomplete adj ON adj state	0	<u>L1</u>